Electronic Acknowledgement Receipt FES ID: 1115231 Application Number: 10779781 Confirmation Number: 1632 Title of Invention: Flat panel display device with polycrystalline silicon thin film transistor First Named Inventor: Ji-Yong Park Customer Number: 58027 Filer: Hae-Chan Park/Wayne Helge Filer Authorized By: Hae-Chan Park Attorney Docket Number: 6161.0111.US Receipt Date: 17-JUL-2006 Filing Date: 18-FEB-2004 Time Stamp: 14:50:25 Application Type: Utility International Application Number: Payment information:

Submitted with Payment File Listing:

ĺ	Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
	1		Reply_filed_07-17-06.pdf	158703	yes	20

no

	manpart besorption					
	Doc Desc	Start	End			
	Amendment - After Non-Final Rejection	1	1			
	Claims	2	12			
	Applicant Arguments/Remarks Made in an Amendment	13	20			
Warnings:						
Information:						

Total Files Size (in bytes):

158703

Multipart Description

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.